

EAST
DOT
10-11-05

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	602	718/107.ccls.	US-PGPUB; USPAT	OR	ON	2005/10/04 14:50
L3	322	718/108.ccls.	US-PGPUB; USPAT	OR	ON	2005/10/04 14:50
L4	278	712/214.ccls.	US-PGPUB; USPAT	OR	ON	2005/10/04 15:07
L5	546	712/228.ccls.	US-PGPUB; USPAT	OR	ON	2005/10/04 15:07
S1	230	712/214.ccls.	US-PGPUB; USPAT	OR	ON	2004/07/15 12:57
S2	427	712/228.ccls.	US-PGPUB; USPAT	OR	ON	2005/10/04 08:41
S3	2	712/228.ccls. and (rester\$3 re-steer\$3)	US-PGPUB; USPAT	OR	ON	2004/02/25 14:35
S4	306	(select\$3 selector multiplexer) with (instruction pointer) with thread	US-PGPUB; USPAT	OR	ON	2004/02/25 14:37
S5	21	((select\$3 selector multiplexer) with (instruction pointer) with thread) and ((suspend\$3 idle inactive stall\$3) with thread with stor\$3 with (pointer instruction))	US-PGPUB; USPAT	OR	ON	2004/02/25 15:10
S6	41	((suspend\$3 idle inactive stall\$3) with thread with stor\$3 with (pointer instruction))	US-PGPUB; USPAT	OR	ON	2004/02/25 16:00
S7	22	kottapalli.in.	US-PGPUB; USPAT	OR	OFF	2004/02/25 15:25
S8	3	thread and (rester\$3 re-steer\$3) same (selector multiplexer mux)	US-PGPUB; USPAT	OR	ON	2004/02/25 16:02
S9	2	(first and second) near2 (buffer queue memory storage cache) same (first and second) near2 thread with active	US-PGPUB; USPAT	OR	ON	2004/03/01 09:57
S10	28	(first and second) near2 (buffer queue memory storage cache) with (instruction pointer) same thread	USPAT	OR	ON	2004/02/25 16:57
S11	6	(mux multiplexer selector) with instruction with (first and second) with thread	USPAT	OR	ON	2004/02/25 16:58
S12	2	thread with stor\$3 with context with separate with (register memory)	US-PGPUB; USPAT	OR	ON	2004/02/26 08:32
S13	14	thread same stor\$3 with context with (separate different) with (register memory)	US-PGPUB; USPAT	OR	ON	2004/02/26 08:32

S14	56	thread same context with (separate different) with (memory register)	US-PGPUB; USPAT	OR	ON	2004/03/01 09:58
S15	19	thread same context with (first and second) with memor\$3	US-PGPUB; USPAT	OR	ON	2004/03/01 09:58
S16	4	"5933627".pn. "5511192".pn. "5127098".pn. "5412788".pn.	US-PGPUB; USPAT	OR	ON	2004/02/26 09:56
S17	0	thread same context with (separate different) with (memory register)	EPO	OR	ON	2004/03/01 09:58
S18	0	thread same context with (separate different) with (memory register)	JPO	OR	ON	2004/03/01 09:58
S19	0	thread same context with (separate different) with (memory register)	DERWENT	OR	ON	2004/03/01 09:58
S20	0	thread same context with (separate different) with (memory register)	IBM_TDB	OR	ON	2004/03/01 09:58
S21	0	thread same context with (first and second) with memor\$3	EPO	OR	ON	2004/03/01 09:58
S22	0	thread same context with (first and second) with memor\$3	JPO	OR	ON	2004/03/01 09:58
S23	1	thread same context with (first and second) with memor\$3	DERWENT; IBM_TDB	OR	ON	2004/03/01 09:58
S24	27	kottapalli.in.	US-PGPUB; USPAT	OR	OFF	2004/11/17 14:44
S25	6	stor\$3 with inactive near4 thread with (pointer instruction)	US-PGPUB; USPAT	OR	ON	2004/11/17 14:45
S26	0	dedicat\$3 with (stor\$3 hold\$3) with (pointer instruction) with (inactive stall\$3 idle) near4 thread	US-PGPUB; USPAT	OR	ON	2005/05/05 14:42
S27	24	(stor\$3 hold\$3) with (pointer instruction) with (inactive stall\$3 idle) near4 thread	US-PGPUB; USPAT	OR	ON	2005/05/05 14:43
S28	13	(multiplex?r selector mux) with (inactive idle dormant sleep\$3) with thread	US-PGPUB; USPAT	OR	ON	2005/10/04 08:49
S29	36	(multiplex?r selector mux) with thread with (pointer)	US-PGPUB; USPAT	OR	ON	2005/10/04 08:50
S30	0	(multiplex?r selector mux) with dedicat\$3 same thread	US-PGPUB; USPAT	OR	ON	2005/10/04 08:49
S31	11	(multiplex?r selector mux) with (inactive idle dormant sleep\$3) with thread	USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/04 08:49

S32	108	(multiplex?r selector mux) with thread with instruction	US-PGPUB; USPAT	OR	ON	2005/10/04 08:51
S33	99	(multiplex?r selector mux) with thread with instruction and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2005/10/04 08:52
S34	6	(multiplex?r selector mux) with thread with (pointer instruction)	USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/04 08:52
S35	45	(first and second) with (multiplex?r selector mux) same thread same instruction and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2005/10/04 09:06
S36	11	("4901230" "4926323" "5113515" "5179702" "5319782" "5357617" "5394529" "5442756" "5490272" "5511175" "5515538").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/04 09:04
S37	47	(first and second) with (multiplex?r selector mux) same thread same (instruction pointer) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2005/10/04 09:07
S38	11	(first and second) near3 (multiplex?r selector mux) same thread same (instruction pointer) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2005/10/04 09:11
S39	17	Kottapalli-Sailesh .in.	US-PGPUB; USPAT	OR	ON	2005/10/04 09:13
S40	13	(thread and (selector mux multiplex?r) and (dedicat\$3 devot\$3 assign\$3) and (inactive idle dormant sleep\$3 switch\$3)).clm.	US-PGPUB	OR	ON	2005/10/04 14:50